



National Institute of Technology Goa

(Institution of National Importance under MHRD, Govt. of India)

Department of Electrical and Electronics Engineering

Advt. No.: NITG/EEE/2019/OW/249

Dated: 02 July, 2019

Advertisement for the Position of Junior Research Fellow (JRF) under DST

Applications are invited from interested and highly motivated candidates for the post of Junior Research Fellow (JRF) to work on the R&D project titled “**Development of coordination control schemes for hybrid AC/DC micro grids for a stable and reliable system operation**”, sanctioned by **Science and Engineering Research Board (SERB), Dept. of Science and Technology (DST), Govt. of India**. JRF will be appointed initially for one year (on contract) and his/her services will be extended for subsequent year(s) based on the performance review. The position is co-terminus with the project.

Name of Project Investigator : Dr. C. Vyjayanthi

Department : Electrical and Electronics Engineering

Duration of the project : 3 Years (2017-2020)

Sl. No.	Position	Basic Qualification	Desirable knowledge areas	Duration	Consolidated Salary	Number of Positions
1	JRF	M.Tech./M.E. Electrical and Electronics/ Electrical Engineering	MATLAB, Micro Controller coding, Digital Signal Processor (DSP)	Initially for the period of one year which may be extended upto maximum of 3 years	Rs. 25,000/- (Per month upto 2 years) and Rs. 28,000 (Per month for 3 rd year) + Other allowances as per Institute norms	01 (One)

Eligibility for JRF:

1. M.Tech./M.E./equivalent in Electrical Engineering/Electrical and Electronics Engineering/Power Electronics/Power Systems with at least 6.5 CGPA or 60 percent marks in aggregate from a recognized technical institute or university as a full time program.



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Desirable Qualification for JRF:

A strong knowledge of MATLAB-Simulink is desirable. Additionally knowledge of hardware implementation/ DSP Controller programming or any controller programming would be highly appreciated.

Project Objectives

- ⇒ Implement a hybrid AC/DC micro grid setup operable both in grid-tied and islanded modes
- ⇒ Develop coordination control algorithms for smooth power transfer between AC and DC microgrids ensuring stable and reliable system operation under various generation and load conditions
- ⇒ Analyze the unintentional/intentional islanding schemes and assessing the performance for different schemes
- ⇒ Analyze active power versus dc voltage droop ($P-V_{dc}$) for the DC microgrid.

Important Instructions:

1. Candidate possessing the requisite qualification and experience should apply; in the attached format along with their updated CV latest by **16th July 2019**. The applicant will be responsible for the authenticity of information, other documents and photographs submitted.
2. Mere, possessing the prescribed qualification does not ensure that the candidate would be called for Interview. The Candidates will be shortlisted on the basis of merit and need of the project.
3. Applicants in employment (private, government or any other organization) are required to submit a “No Objection Certificate” from the employer at the time of interview.
4. Duly filled and signed scan copy of Application Form along with the scanned copies of mark sheets/documents must be sent to Dr. C. Vyjayanthi (PI), **only through e-mail**, at: c.vyjayanthi@nitgoa.ac.in with subject line “**Application for JRF position for AC-DC Project in the Department of EEE for SERB Project**”.
5. The Shortlisted Candidates will be informed by e-mail (apart from website) along with the date and time of the written test/interview. No other letter will be sent to the correspondence address. So, the candidates are advised to check their email regularly.
6. Shortlisted candidates have to present themselves for the written test and interview with updated CV, application form, original and attested photocopies of mark sheets/certificates in support of their academic qualifications.



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7. No TA/DA shall be paid to candidates for attending the Interview and/or joining the position.
8. The appointment is for a time bound project and the candidate is required to work dedicatedly for the successful completion of the project. Selected candidate has to join immediately.
9. Incomplete application forms and forms received after due date will be summarily rejected.
10. All the Terms and Conditions for this recruitment will be as per guidelines of SERB, DST, Govt. of India.

Application Process

Application form (as given below) giving all the details and copies of certificates, supporting documents and experience should be mailed to the undersigned latest by **16th July 2019**. Candidates who are already employed should produce relieving certificate from their employers, if selected. The written test/interview will be conducted for all the eligible candidates. The applicants need to bring all the original documents for verification during written test/interview. The list of shortlisted candidates will be posted in the NIT Goa website (www.nitgoa.ac.in) along with the date and time of the written test/interview.

-Sd-

Dr. C.Vyjayanthi

Email: c.vyjayanthi@nitgoa.ac.in



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Application for the Position of JRF (Junior Research Fellow) under DST SERB

Title of Project: Development of coordination control schemes for hybrid AC/DC micro grids for a stable and reliable system operation.

1. Post Applied for : Junior Research Fellow

2. Name of the Candidate (BLOCKLETTER): _____

3. Father's Name (BLOCKLETTER): _____

4. Mother's Name (BLOCKLETTER): _____

5. (a) Date of Birth: (DD/MM/YYYY) _____

(b) Sex (Male/Female/Other): _____

(c) Marital Status (Married/Single): _____

(d) Category (SC/ST/OBC/PWD/GEN): _____

6. Previous Research experience: (use additional sheet if required) _____

7. Publication(s), if any: (use additional sheet if required) _____

8. GATE/ NET (if any): Qualified (Yes/No): Score: _____ Rank: _____

Specialization: _____ Year: _____

9. Academic Qualification: (Starting from Standard 10 or equivalent Examination)

Name of Exam Passed	Name of the School/College/Institute/University	Year of Passing	Discipline/Specialization	Percentage of Marks/CGPA

Paste here a recent Passport size Photograph



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10. (a) Address for Communication: (BLOCKLETTER)

(b) Contact No. (Mobile)

(c) E-mail ID :

11. Contact Details of two referees:

	Referee I	Referee II
Name :		
Designation :		
Organization:		
Office Address :		
Office Phone Number:		
Email ID:		

12. Experience details:

I do here by declare that the information furnished in this application is true to the best of my knowledge and belief. If selected, I promise to abide by the rules and regulations of the Institute.

Date:

Place:

Signature of the candidate