



राष्ट्रीय प्रौद्योगिकी संस्थान गोवा
NATIONAL INSTITUTE OF TECHNOLOGY GOA

Farmagudi, Ponda, Goa - 403 401, India

Department of Electrical and Electronics Engineering

Dr. Soumitra Das
Email-id: sdas@nitgoa.ac.in

Phone : 0832-2404218
Fax : 0832-2404202

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Advertisement for the Position of Junior Research Fellow (JRF) under DST

Applications are invited from interested and highly motivated candidates for the post of Junior Research Fellow (JRF) to work on a Research and Development project titled “**Design of Superior Pulsewidth Modulation Schemes for High-Performance Multilevel Converter based Grid-Connected Photovoltaic System**”, sanctioned by **Science and Engineering Research Board (SERB), Dept. of Science and Technology (DST), Govt. of India**. JRF will be appointed initially for one year (on contract) and his/her services will be extended for subsequent year(s) based on the performance review. The position is co-terminus with the project.

Name of Project Investigator : Dr. Soumitra Das
Department : Electrical and Electronics Engineering
Duration of the project : 3 Years (2017-2020)

Position	Basic Qualification	Desirable knowledge areas	Duration	Consolidated Salary	Number of Positions
JRF	M.Tech./M.E./ B.Tech./B.E. in Electrical and Electronics/ Electrical Engineering	MATLAB, Micro Controller coding, Digital Signal processor (DSP)	Initially for the period of one year which may be extended upto maximum of 3 years	Rs. 25,000/- (Per month upto 2 years), Rs. 28,000 (Per month for 3 rd year)	01 (One)

Eligibility for Project Associate/JRF:

- M.Tech./M.E./equivalent** in Electrical Engineering / Electrical and Electronics Engineering with at least 6.5 CGPA or 60 percent marks in aggregate from a recognized technical institute or university as a full time program.
- B. Tech/B.E./** in Electrical Engineering / Electrical and Electronics Engineering with at least 6.5 CGPA or 60 percent marks in aggregate from a recognized technical institute or university as a full time program.

Desirable Qualification for Project Associate/JRF:

A strong knowledge of MATLAB-Simulink is highly desirable. Additionally knowledge in Grid-tied PV System, MPP Techniques, Multi-level Inverters, and DSP Controller programming would be appreciated.

Important Instructions:

1. Candidate possessing the requisite qualification and experience should apply on line (<https://goo.gl/forms/67Xxnhv3PdJyfNpD2>) latest by **30th October 2017**. **The candidate must not send any copy of duly filled application form through personal mail to Dr. Soumitra Das.** The applicant will be responsible for the authenticity of information, other documents and photographs submitted.
2. Mere, possessing the prescribed qualification does not ensure that the candidate would be called for Interview. The Candidates will be shortlisted on the basis of merit and need of the project.
3. Applicants in employment (private, government or any other organization) are required to submit a “No Objection Certificate” from the employer at the time of interview.
4. The Shortlisted Candidates will be informed by e-mail (apart from website) along with the date and time of the written test/interview. No other letter will be sent to the correspondence address. So, the candidates are advised to check their email regularly.
5. Shortlisted candidates have to present themselves for the interview with updated CV, application form, original and attested photocopies of mark sheets/certificates in support of their academic qualifications.
6. No TA/DA shall be paid to candidates for attending the Interview and/or joining the position.
7. The appointment is for time bound project and the candidate is required to work dedicatedly for the successful completion of the project. Selected candidate has to join immediately.
8. Incomplete application forms and forms received after due date will be summarily rejected.
9. All the Terms and Conditions for this recruitment will be as per guidelines of DST SERB, Govt. of India.

Application Process

Candidate possessing the requisite qualification and experience should apply on line (<https://goo.gl/forms/67Xxnhv3PdJyfNpD2>) latest by **30th October 2017**. **The candidate must not send any copy of duly filled application form through personal mail to Dr. Soumitra Das.** Candidates who are already employed should produce relieving certificate from their employers, if selected. The written test and/or interview will be conducted for all the eligible candidates. The applicants need to bring all the original documents for verification during written test and/or interview. The list of shortlisted candidates will be posted in the NIT Goa website (www.nitgoa.ac.in) along with the date and time of the written test and/or interview.

If you have any queries, please contact Dr. Soumitra Das through email-id only (sdas@nitgoa.ac.in).



Dr. Soumitra Das,
Assistant Professor,
Dept. of EEE